Vishay Semiconductors

High Performance Schottky Rectifier, 3.0 A



www.vishay.com



DO-214AB (SMC)

PRODUCT SUMMARY					
Package	DO-214AB (SMC)				
I _{F(AV)}	3.0 A				
V _R	40 V				
V _F at I _F	0.46 V				
I _{RM}	30 mA at 125 °C				
T _J max.	150 °C				
Diode variation	Single die				
E _{AS}	6.0 mJ				

FEATURES

- Very low forward voltage drop
- Guard ring for enhanced ruggedness and long RoHS compliant reliability
- Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-30BQ040HM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UNIT					
I _{F(AV)}	Rectangular waveform	3.0	A				
V _{RRM}		40	V				
I _{FSM}	t _p = 5 μs sine	1600	A				
V _F	3.0 A _{pk} , T _J = 125 °C	0.46	V				
Тј	Range	-55 to +150	۵°C				

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-30BQ040HM3	UNITS			
Maximum DC reverse voltage	V _R	40	V			
Maximum working peak reverse voltage	V _{RWM}	40	v			

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average forward average		50 % duty cycle at T_L = 115 °C,	3.0			
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T_L = 104 °C, r	4.0			
Maximum peak one cycle	I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	1600	A	
non-repetitive surge current		10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	90		
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1.0 A, L = 12 mH		6.0	mJ	
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _B typical		1.0	А	

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FREE

VS-30BQ040HM3



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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS		
		3 A	T.I = 25 °C	0.57	V	
Maximum forward valtage drep	V (1)	6 A	$-1_{\rm J} = 25$ C	0.76		
Maximum forward voltage drop	V _{FM} ⁽¹⁾	3 A	T 405.00	0.46		
		6 A	– T _J = 125 °C	0.64		
Maximum reverse leakage current		T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	0.5	mA	
Maximum reverse leakage current	I _{RM}	T _J = 125 °C	$v_{\rm R} = Rated v_{\rm R}$	30	mA	
Maximum junction capacitance	CT	$V_{R} = 5 V_{DC}$ (test signal rar	230	pF		
Typical series inductance	L _S	Measured lead to lead 5	3.0	nH		
Maximum voltage rate of change	dV/dt	Rated V _R 10 000			V/µs	

Note

 $^{(1)}\,$ Pulse width = 300 $\mu s,$ duty cycle = 2 $\,\%$

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-55 to +150	°C	
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾		12	°C/W	
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	46		
Approvimate weight			0.24	g	
Approximate weight			0.008	oz.	
Marking device		Case style SMC (similar to DO-214AB)	3F	-	

Notes

(1)

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

(2) Mounted 1" square PCB



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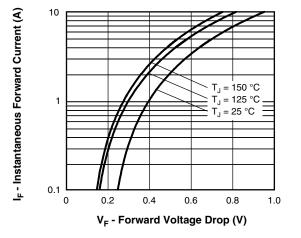


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

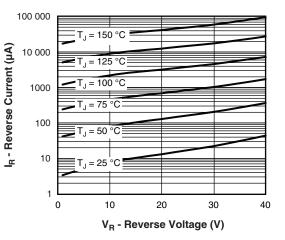


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

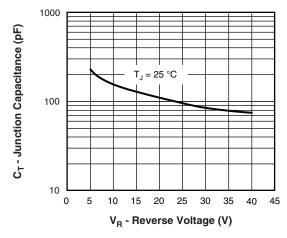


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

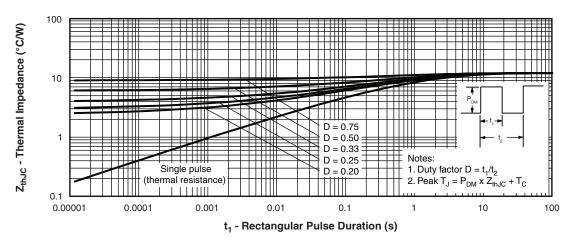


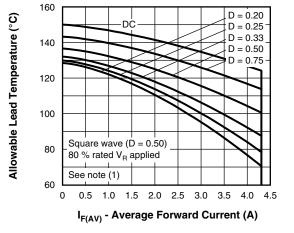
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

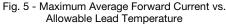
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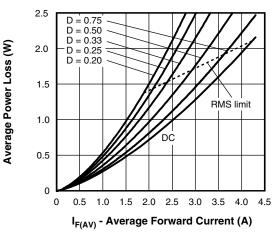


Fig. 6 - Maximum Average Forward Dissipation vs. Average Forward Current

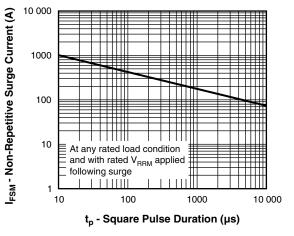


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$; $I_R at V_{R1} = 80 \%$ rated V_R

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(Pb)-free



ORDERING INFORMATION TABLE

Device code	vs-		30	В	Q	040	н	М3
	1	. (2	3	4	5	6	7
	1	-	Visł	nay Sem	niconduc	ctors pro	oduct	
	2	-	Cur	rent rati	ng			
	3	-	В=	SMC				
	4	-	Q =	Schottk	ky "Q" se	eries		
	5	-	Volt	tage rati	ng (040	= 40 V))	
	6	-	H =	AEC-Q	101 qua	lified		
	7	-	Env	rironmer	ntal digit	:		
			М3	= haloge	en-free,	RoHS-o	complia	nt, and

ORDERING INFORMATION (Example)							
PREFERRED P/N	PREFERRED PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-30BQ040HM3/9AT	9AT	3500	13" diameter plastic tape and reel				

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95402			
Part marking information	www.vishay.com/doc?95403			
Packaging information	www.vishay.com/doc?95404			

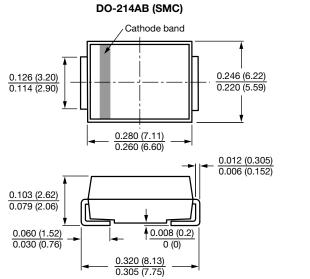


Outline Dimensions

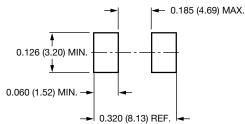
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DIMENSIONS in inches (millimeters)



Mounting Pad Layout





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