

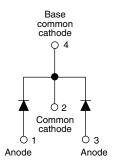
COMPLIANT

HALOGEN

FREE

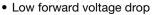
Schottky Rectifier, 2 x 6 A

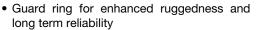


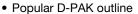


PRODUCT SUMMARY				
Package	D-PAK (TO-252AA)			
I _{F(AV)}	2 x 6 A			
V_{R}	60 V			
V _F at I _F	0.57 V			
I _{RM}	35 mA at 125 °C			
T _J max.	150 °C			
Diode variation	Common cathode			
E _{AS}	7 mJ			

FEATURES







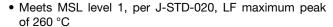
Center tap configuration

• Small foot print, surface mountable

• High frequency operation

AEC-Q101 qualified

· Meets JESD 201 class 2 whisker test



• Material categorization: For definitions of compliance please see www.vishay.com/doc?99912



DESCRIPTION

The VS-12CWQ06FNHM3 surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	12	A		
V_{RRM}		60	V		
I _{FSM}	t _p = 5 µs sine	320	A		
V _F	6 A _{pk} , T _J = 125 °C (per leg)	0.57	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-12CWQ06FNHM3	UNITS		
Maximum DC reverse voltage	V_{R}	60	V		
Maximum working peak reverse voltage	V_{RWM}	00	V		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average per leg		50 % duty cycle at T _C = 131 °C, rectangular waveform		6	А
See fig. 5 per device	$I_{F(AV)}$ 50 % duty cycle at I_C = 131 °C, rectangular waveform		12	A	
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	320	А
non-repetitive surge current See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse		105	A
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 1.2 A, L = 10 m	nH	7	mJ
Repetitive avalanche current per leg $I_{AR} \qquad \text{Current decaying linearly to zero in 1 } \mu s \\ \text{Frequency limited by T}_{J} \text{ maximum V}_{A} = 1.5 \text{ x V}_{R} \text{ typical}$		0.8	Α		



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	6 A	T _J = 25 °C	0.61	V
Maximum forward voltage drop per leg		12 A		0.79	
See fig. 1		6 A	T _J = 125 °C	0.57	
551.19.1		12 A		0.72	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	- V _R = Rated V _R	3	- mA
See fig. 2	'RM ` ′	T _J = 125 °C		35	
Threshold voltage	V _{F(TO)}	$T_{\rm J} = T_{\rm J} \text{maximum}$ 0.36 24.14		0.36	V
Forward slope resistance	r _t			mΩ	
Typical junction capacitance per leg	C _T	V _R = 5 V _{DC} , (test signal range 100 kHz to 1 MHz), 25 °C 360 p		pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 5.0 nH		nH	

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J ⁽¹⁾ , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance,	per leg	D	DC operation	3.0	°C/W
junction to case	per device	R_{thJC}	See fig. 4	1.5	C/ VV
Approximate weight				0.3	g
Approximate weight				0.01	OZ.
Marking device			Case style D-PAK	12CWC	06FNH

Note

$$^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$$



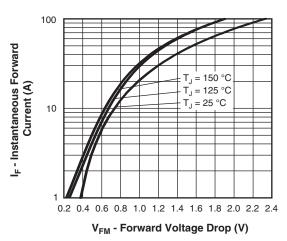


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

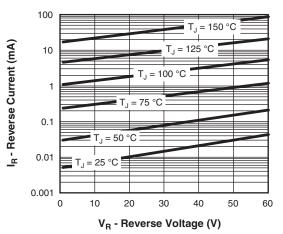


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

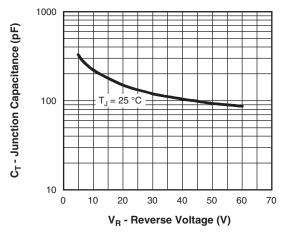


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

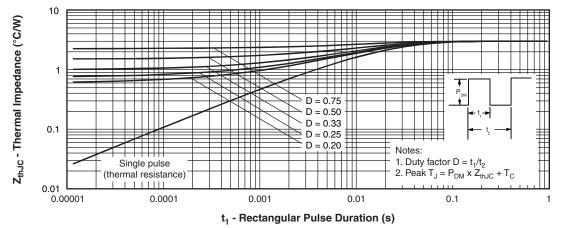
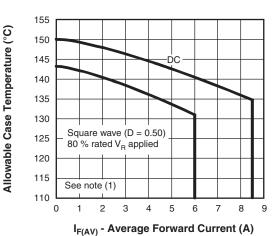


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)



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Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

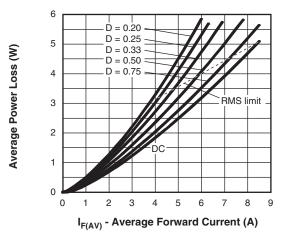


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

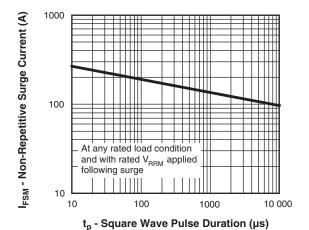


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

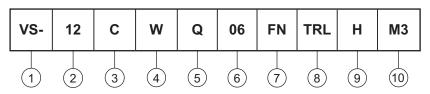
Note

 $^{(1)}$ Formula used: $T_{C} = T_{J}$ - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = $I_{F(AV)}$ x V_{FM} at ($I_{F(AV)}/D$) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (12 A)

Center tap configuration

Package identifier:

W = D-PAK

5 - Schottky "Q" series

Voltage rating (06 = 60 V)

7 - FN = TO-252AA

8 - • None = Tube

• TR = Tape and reel

• TRL = Tape and reel (left oriented)

• TRR = Tape and reel (right oriented)

9 - H = AEC-Q101 qualified

10 - Environmental digit:

M3 = Halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)				
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION	
VS-12CWQ06FNHM3	75	3000	Antistatic plastic tube	
VS-12CWQ06FNTRHM3	2000	2000	13" diameter reel	
VS-12CWQ06FNTRRHM3	3000	3000	13" diameter reel	
VS-12CWQ06FNTRLHM3	3000	3000	13" diameter reel	

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95519</u>				
Part marking information <u>www.vishay.com/doc?95518</u>				
Packaging information	www.vishay.com/doc?95033			



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