

# DATASHEET

# 4 PIN DIP RANDOM-PHASE TRIAC DRIVER PHOTOCOUPLER ELT302X, ELT305X Series



#### Features:

- Peak breakdown voltage
- 400V: ELT302X
- 600V: ELT305X
- High isolation voltage between input and output (Viso=5000 V rms)
- Compact dual-in-line package
- Pb free and RoHS compliant.
- UL and cUL approved(No. E214129)
- VDE approved (No. 40028391)
- SEMKO approved
- NEMKO approved
- DEMKO approved
- FIMKO approved

#### Description

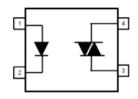
The ELT302X and ELT305X series of devices each consist of a GaAs infrared emitting diode optically coupled to a monolithic silicon random phase photo Triac.

They are designed for interfacing between electronic controls and power triacs to control resistive and inductive loads for 115 to 240 VAC operations.

# **Applications**

- Solenoid/valve controls
- Lamp ballasts
- Static AC power switch
- Interfacing microprocessors to 115 to 240Vac peripherals
- Incandescent lamp dimmers
- Temperature controls
- Motor controls

#### **Schematic**



Pin Configuration

- 1. Anode
- 2. Cathode
- 3. Terminal
- 4. Terminal

# Absolute Maximum Ratings (Ta=25°C)

	Parameter		Symbol	Rating	Unit	
Input	Forward current		I <sub>F</sub>	60	mA	
	Reverse voltage		V <sub>R</sub>	6	V	
	Power dissipation		_	100	mW	
	Derating factor (above $T_a = 85^{\circ}C$ )		P <sub>D</sub> -	3.8	mW /°C	
Output	Off-state Output Terminal Voltage	ELT302X		400		
		ELT305X	– V <sub>drm</sub> –	600	- V	
	Peak Repetitive Surge Current		I <sub>TSM</sub>	1	А	
	Power dissipation		5	300	mW	
	Derating factor (above $T_a = 85^{\circ}C$ )		P <sub>C</sub> -	7.4	mW/°C	
Total power dissipation		P <sub>TOT</sub>	330	mW		
Isolation voltage <sup>*1</sup>			V <sub>ISO</sub>	5000	Vrms	
Operating temperature			T <sub>OPR</sub>	-55 to 100	°C	
Storage temperature		T <sub>STG</sub>	-55 to 125	°C		
Soldering Temperature* <sup>2</sup>			T <sub>SOL</sub>	260	°C	

Notes:

\*1 AC for 1 minute, R.H.= 40 ~ 60% R.H. In this test, pins 1 & 2 are shorted together, and pins 3 & 4 are shorted together.

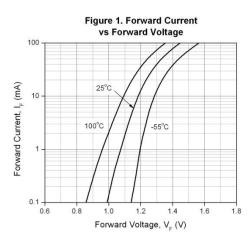
\*2 For 10 seconds

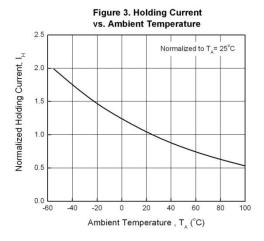
# Electro-Optical Characteristics (Ta=25°C unless specified otherwise)

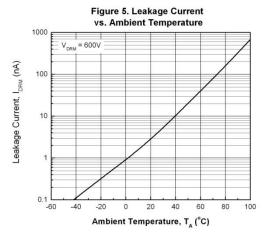
Input							
Parameter		Symbol	Min.	Тур.*	Max.	Unit	Condition
Forward Voltage	Э	V <sub>F</sub>	-	1.18	1.5	V	I <sub>F</sub> = 10mA
Reverse Leaka	ge current	I <sub>R</sub>	-	-	10	μΑ	$V_R = 6V$
Output							
Param	eter	Symbol	Min.	Тур.*	Max.	Unit	Condition
Peak Blocking Current		I <sub>DRM</sub>	-	-	100	nA	$V_{DRM} = Rated V_{DRM}$ I <sub>F</sub> = 0mA
Peak On-state Voltage		V <sub>TM</sub>	-	-	2.5	V	I <sub>TM</sub> =100mA peak, I <sub>F</sub> =Rated I <sub>FT</sub>
Critical Rate of	ELT302X	als / alt	-	100	-	V/µs	V <sub>PEAK</sub> =Rated V <sub>DRM</sub> , I <sub>F</sub> =0 (Fig. 8)
Rise off-state Voltage	ELT305X	- dv/dt ·	1000	-	-		V <sub>PEAK</sub> =400V, I <sub>F</sub> =0 (Fig. 8)
Transfer Cha	racteristics						
Param	eter	Symbol	Min.	Тур.*	Max.	Unit	Condition
	ELT3021 ELT3051		-	-	15		
LED Trigger Current	ELT3022 ELT3052	I <sub>FT</sub>	-	-	10	mA	Main terminal Voltage=3V
	ELT3023 ELT3053		-	-	5		
Holding Currer	nt	Ι <sub>Η</sub>	-	250	-	μA	

\* Typical values at T<sub>a</sub> = 25°C

# **Typical Electro-Optical Characteristics Curves**







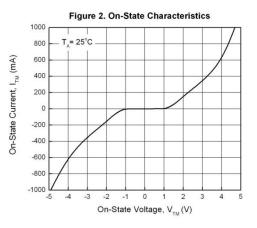
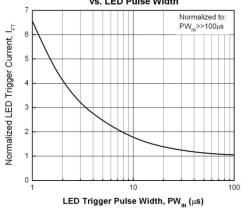


Figure 4. LED Current Required to Trigger vs. LED Pulse Width



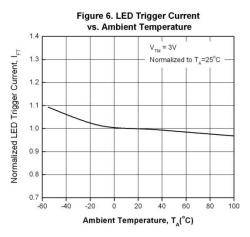
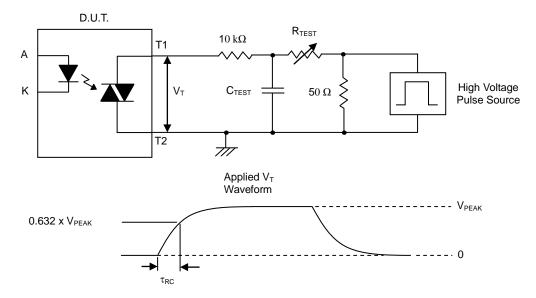


Figure 7. Off-State Output Terminal Voltage vs. Ambient Temperature 1.4 Normalized to T<sub>A</sub>=25°C 1.3 Normalized Off-State Output Terimal Valtage, V<sub>DRM</sub> 1.2 1.1 1.0 0.9 0.8 0.7 🖵 -60 -40 -20 20 40 60 80 100 Ambient Temperature, T<sub>A</sub> (°C)

Figure 8. Static dv/dt Test Circuit & Waveform



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#### **Measurement Method**

The high voltage pulse is set to the required V<sub>PEAK</sub> value and applied to the D.U.T. output side through the RC circuit above. LED current is not applied. The waveform V<sub>T</sub> is monitored using a x100 scope probe. By varying R<sub>TEST</sub>, the dv/dt (slope) is increased, until the D.U.T. is observed to trigger (waveform collapses). The dv/dt is then decreased until the D.U.T. stops triggering. At this point,  $\tau_{RC}$  is recorded and the dv/dt calculated.

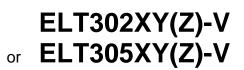
$$dv/dt = \frac{0.632 \text{ x } V_{\text{PEAK}}}{\tau_{\text{RC}}}$$

For example,  $V_{PEAK}$  = 400V for EL302X series. The dv/dt value is calculated as follows:

$$dv/dt = \frac{0.63 \times 400}{\tau_{RC}} = \frac{252}{\tau_{RC}}$$

# **Order Information**

Part Number



#### Note

X = Part No. (1, 2 or 3)

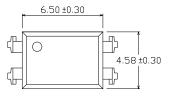
- Y = Lead form option (S, S1, M or none)
- Z = Tape and reel option (TA, TB, TU, TD or none).

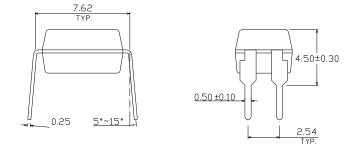
V = VDE safety approved (optional)

Option	Description	Packing quantity
None	Standard DIP-4	100 units per tube
М	Wide lead bend (0.4 inch spacing)	100 units per tube
S (TA)	Surface mount lead form + TA tape & reel option	1000 units per reel
S (TB)	Surface mount lead form + TB tape & reel option	1000 units per reel
S1 (TA)	Surface mount lead form (low profile) + TA tape & reel option	1000 units per reel
S1 (TB)	Surface mount lead form (low profile) + TB tape & reel option	1000 units per reel
S (TU)	Surface mount lead form + TU tape & reel option	1500 units per reel
S (TD)	Surface mount lead form + TD tape & reel option	1500 units per reel
S1 (TU)	Surface mount lead form (low profile) + TU tape & reel option	1500 units per reel
S1 (TD)	Surface mount lead form (low profile) + TD tape & reel option	1500 units per reel

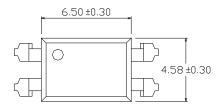
# Package Dimension (Dimensions in mm)

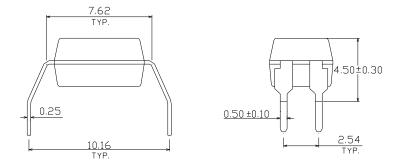
#### **Standard DIP Type**



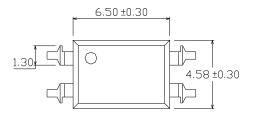


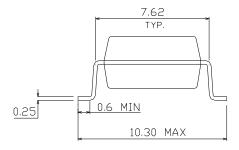
#### **Option M Type**

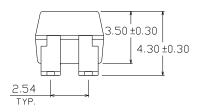




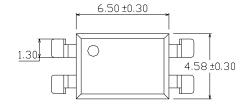
#### **Option S Type**

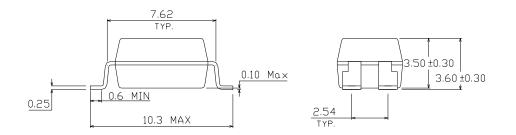






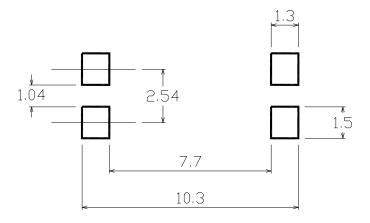
#### **Option S1 Type**







#### Recommended pad layout for surface mount leadform



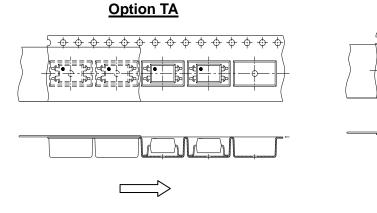
# **Device Marking**



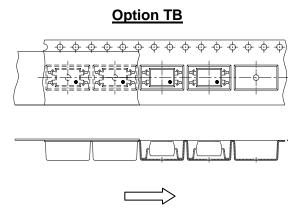
#### Notes

EL	denotes Everlight
T3053	denotes Device Number
Y	denotes 1 digit Year code
WW	denotes 2 digit Week code
V	denotes VDE option

# **Tape & Reel Packing Specifications**



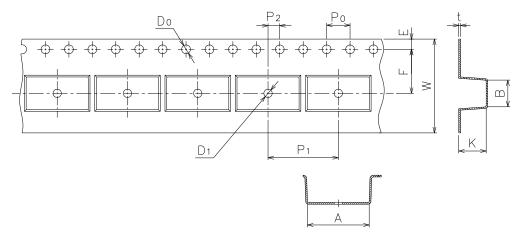
Direction of feed from reel



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Direction of feed from reel

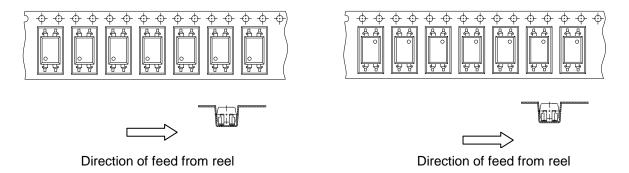
## **Tape dimensions**



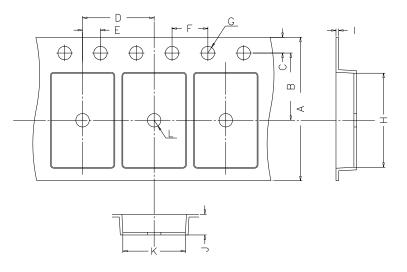
Dimension No.	Α	В	Do	D1	Е	F
Dimension (mm)	10.5±0.1	4.65±0.1	1.55±0.1	1.50±0.1	1.75±0.1	7.5±0.1
Dimension No.	Ро	P1	P2	t	w	к
Dimension (mm)	4.0±0.1	12.0±0.1	2.0±0.1	0.35±0.1	16.0±0.3	4.75±0.1

# Option TD

## Option TU



#### **Tape dimensions**

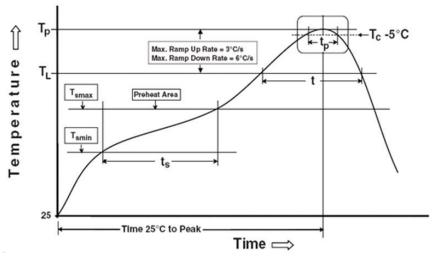


Dimension No.	Α	В	С	D	E	F
Dimension(mm)	16.00±0.3	7.5±0.1	1.75±0.1	8.0±0.1	2.0±0.1	4.0±0.1
Dimension No.	G	н	I	J	К	L
Dimension(mm)	1.55±0.05	10.4±0.1	0.4±0.05	4.60±0.1	5.1±0.1	1.55±0.05

# **Precautions for Use**

#### 1. Soldering Condition

1.1 (A) Maximum Body Case Temperature Profile for evaluation of Reflow Profile



Note:

#### Preheat

Temperature min (T <sub>smin</sub> )	150 °C
Temperature max (T <sub>smax</sub> )	200°C
Time ( $T_{smin}$ to $T_{smax}$ ) ( $t_s$ ) Average ramp-up rate ( $T_{smax}$ to $T_p$ )	60-120 seconds 3 °C/second max
Other	
Liquidus Temperature (TL)	217 °C
Time above Liquidus Temperature (t $_{L}$ )	60-100 sec
Peak Temperature (T <sub>P</sub> )	260°C
Time within 5 °C of Actual Peak Temperature: $T_{P}$ - 5°C	30 s
Ramp- Down Rate from Peak Temperature	6°C /second max.
Time 25°C to peak temperature Reflow times	8 minutes max. 3 times

Reference: IPC/JEDEC J-STD-020D

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