

Shipped in packet-tape reel(5000pcs/Reel)

EM-1791 is ultra-small Hall effect ICs of a single silicon chip composed of Hall element and a signal processing IC.

Unipolar Hall Effect Switch Two output for S and N-pole

Supply Voltage 1.6~5.5V

Hall Element Pulse Excitation

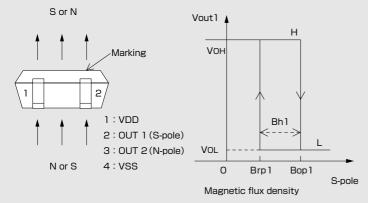
High Sensitivity Bop:2.5mT

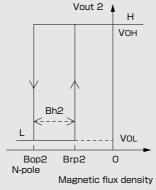
Output CMÓS Two output for S and N-pole

SMT

Notice: It is requested to read and accept "IMPORTANT NOTICE" written on the back of the front cover of this catalogue.

Operational Characteristics



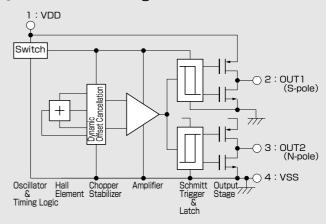




● Absolute Maximum Ratings (Ta=25°C)

Item	Symbol	Limit	Unit	
Supply Voltage	VDD	−0.1 ~ 6.0	V	
Output Current	I _{out}	±0.5	mA	
Operating Temperature Range	Topr	−30 ~ + 85	°C	
Storage Temperature Range	Tstg	−40 ~ +125	င	

Functional Block Diagram



Item	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Supply Voltage	VDD		1.6		5.5	٧	
Operating Point	B _{OP} 1		*1.4	2.5	3.2	mT	
	B _{OP} 2		-3.2	-2.5	*-1.4		
Release Point	B _{rp} 1		1.2	2.0	*3.0	mT	
	B _{rp} 2		*-3.0	-2.0	-1.2		
Hysteresis	B _h 1,B _h 2			0.5		mT	
Period	Тр			50	100	ms	
Output High Voltage	Vон	Io=-0.2mA	VDD-0.4			V	
Output Low Voltage	VoL	Io=+0.2mA			0.4	V	
Supply Current	IDD	Average		6.5	9	μΑ	

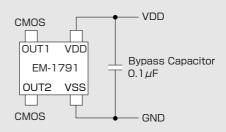
1 [mT] =10 [Gauss]

●Magnetic ① and Electrical Characteristics (Ta=25°C VDD=1.85V) ●Magnetic Characteristics ② (Ta=-30~+85°C VDD=1.85V)

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Operating Point	B _{OP} S B _{OP} N		1.3	2.5	3.5	mT
Release Point	B _{rp} S B _{rp} N		1.1	2.0	3.3	mT
Hysteresis	B _h S B _h N			0.5		mT

Note) The above specifications are design targets.

Application Circuit



The characteristics with $\lceil * \rfloor$ marks are design targets. OUT1 responds to the positive flux from the south pole(Bop1,Brp1),OUT2 to the negative flux from the north pole(Bop2,Brp2) .

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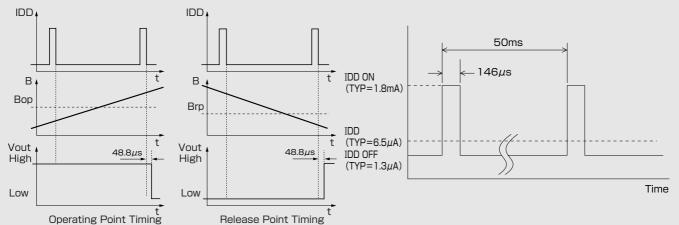
Package (Unit:mm) ●(For reference only)Land Pattern (Unit:mm) 2.1±0.1 0.25 03 0.50 φ0.3 2.1±0.2 25±0. 0~0.1 0.90 Sensor center 3 90 0.05 0.1 Note 1) The sensor center is located within the ϕ 0.3mm circle. Note2) The tolerances of dimensions ന with no mentions is ± 0.1 mm. Note3) Coplanarity: The differnces between standoff of terminals are max.0.1mm. 1.30 Note4) The sensor part is located 0.4mm(typ.) Pin No. | Pin Name | Function | Comment far from marking surface. VDD Supply Voltage OUT 1 Output Voltage S-pole OUT2 Output Voltage N-pole

Function Timing Chart

GND

VSS

●IDD Pulse Driving (VDD=1.85V)

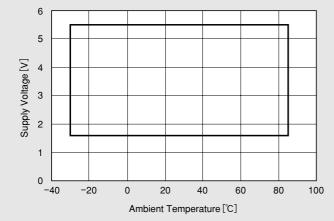


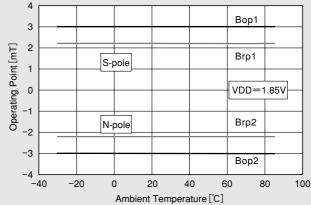
This Hall IC's output is held as internal data just before the internal circuit turns OFF (IDD OFF). And after 48.8 $\,\mu$ s, the output changes.

Supply Voltage

Note) 48.8 us in figures is typical value

●Temparature Dependence of Bop. Brp





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